



PTO/SB/21 (05-03)

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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/607,633	
	Filing Date	June 27, 2003	
	First Named Inventor	JOSHI, RAJEEV	
	Art Unit	Unassigned	
	Examiner Name	Unassigned	
Total Number of Pages in This Submission	3	Attorney Docket Number	018865-004210US

ENCLOSURES (Check all that apply)		
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Firm or Individual	Townsend and Townsend and Crew LLP Patrick R. Jewik Reg. No. 40,456
Signature	
Date	9/9/03

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Signature		Date	9/9/03

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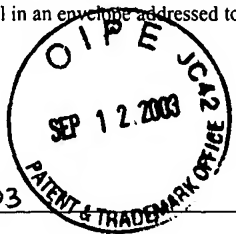
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On September 9, 2003

TOWNSEND and TOWNSEND and CREW LLP

By: 



PATENT
Attorney Docket No.: 018865-004210US
Client Reference No.: 17732.13420.003
(divisional)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Joshi et al.

Application No.: 10/607,633

Filed: June 27, 2003

For: FLIP CHIP IN LEADED MOLDED
PACKAGE AND METHOD OF
MANUFACTURE THEREOF

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98

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P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. A copy of reference AN, "IR's New Synchronous Rectifier Chip...", is enclosed. In accordance with 37 CFR §1.98(d), copies of the remaining references can be found in Application No. 09/464,885, filed December 16, 1999 (Attorney Docket No. 018865-004200US). It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

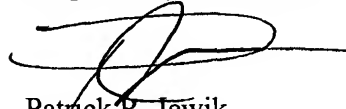
As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement.

However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



Patrick R. Jewik
Reg. No. 40,456

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Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/607,633
Filing Date	June 27, 2003
First Named Inventor	JOSHI, RAJEEV
Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	018865-004210US

U.S. PATENT DOCUMENTS+

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ² (if known)			
	AA	US-5,105,536	04-21-1992	Neugebauer et al.	
	AB	US-5,250,841	10-05-1993	Sloan et al.	
	AC	US-5,319,242	06-07-1994	Carney et al.	
	AD	US-6,198,163	03-06-2001	Crowley et al.	
	AE	US-6,215,176	04-10-2001	Huang	
	AF	US-6,307,755	10-23-2001	Williams et al.	
	AG	US-6,384,492	05-07-2002	Iversen et al.	
	AH	US-6,399,418	06-04-2002	Glenn et al.	
	AI	US-6,448,110	09-10-2002	Chen et al.	
	AJ	US-6,452,278	09-17-2002	DiCaprio et al.	
	AK	US-6,528,880	03-04-2003	Planey	
	AL	US-2003/0025183	02-06-2003	Thomton et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
	AM	JP	1-134958	A	05-26-1989	Sato et al.		<input type="checkbox"/>

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AN	"IR'S New Synchronous Rectifier Chip Set Meets New Efficiency Standard for DC-DC Converters to Power Notebook PC Processors through 2000." International Rectifier Company Information. Retrieved from the World Wide Web on September 9, 2003 at http://www.irf.com/whats-new/nr990402.html .	

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Considered

¹ EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

² Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.